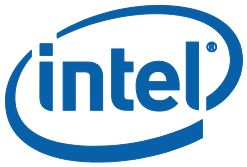

Ice Lake Platform Intel® MG PHY Firmware

Release Notes - NDA

Revision 9.0.2.0

January 2020

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Revision History

Revision Number	Description	Revision Date
9.0.0.1	Initial Release	Jan 2019
9.0.0.5	ES2 Supp Release	March 2019
9.0.0.9	QS Release	April 2019
9.0.1.1	Release	May 2019
9.0.1.3	PV Release	May 2019
9.0.1.4	PR1 Release	July 2019
9.0.1.5	PR2 Release	September 2019
9.0.1.6	PR2 Release	September 2019
9.0.2.0	PR2 Release	February 2020

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1 Introduction

1.1 Scope of Document

This document provides component level details of the downloaded kit and the contents of each folder in the kit.

1.2 Acronyms

Term	Description
BIOS	Basic Input Output System
FW	Firmware

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2 Issue Status Definitions

This document provides sightings and bugs report for Intel® CSME Firmware 13.0 SKU for the Ice Lake Platform. At the time of a milestone release, this report will be distributed with the Intel® CSME Kit and will provide information on new issues and the status of old issues (replacing the Release Notes document).

Closed Issues: This category will only display closed issues within the current Intel® CSME Kit release. After each release, old issues will be dropped down to the "Archive" section and then new closed issues will take its place back up top for the next release. If an issue is posted in this section, it will indicate that the issue has been verified and fixed within the kit that is being released.

Known Issues: This category will display all Known Issues since the Alpha release and will remain in this section until fixed or noted otherwise. "Known Issues" are still under investigation and may or may not be root caused.

Archive – Fixes in Previous Kits: This category will display all closed issues that were closed in their respected kit#. This section will serve as a history of fixed issues.

Sightings listed in this document apply to the Ice Lake SKU's unless noted otherwise in this document or in the sightings tracking systems.

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3 Intel® MG PHY New Features - RCR's

RCR Number	Change Information

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4 Closed Issues in this Release

Issue Number	Description	Details	Fixed in Version
1306156818	USB-C Gen2 device may downgrade to USB2 while exing from low power link state	While XHCI controller requesting to exit from low power link state the USB link may fail and cause downgrade to USB2	9.0.0.5
1306252941	Thunderbolt link may get stuck in training after plug unplug Thunderbolt device.	Hang may occur while Thunderbolt Dissconnect/connect when the processor is in PKG-C10	9.0.0.5
2207221275	USB 3.1 Gen2 devices may sporadically gets downgraded to USB 2	USB Superspeed+ devices can be downgraded to Highspeed while connected to hub and waking from S4 state	9.0.0.9
-	Voltage I/O margining enabled in this MGPHY FW version – it must be align with IOM FW Release version: 04.008.0.00	-	9.0.1.1
1807331009	TCSS Link training may fail when plug in device	TCSS Link training may fail when TBT/USB3.1/DP device plugged into Type-C port and Processor runs at temperature >90c.	9.0.1.3
1807444907	Compliance RX loopback may fail	USB 3.1 GEN 2 RX may fail to enter loopback mode during compliance tests on direct USB port	9.0.1.4
1607395752	USB device may not re-enumerate	USB device may not re-enumerate while data transfer in S0 or hot plug and unplug using TBT Dock in S0	9.0.1.5
1607207567	TBT or USB3.1 devices may lost enumeration	TBT/USB3.1 devices may lost enumeration during S3/S4/CS cycling	9.0.1.6
1307014595	TBT port may have low receiver margin	Tuning TCSS PHY receiver configuration to improve margins	9.0.2.0

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5 Open / Known Issues – To Date

Issue Number	Description	Details	Affected Sku's

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6 Archive

6.1 Fixed in Previous Kits

N/A

6.2 RCR’s

N/A

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